

06 MAY 15
Roderick.

HW PWM ON PICAXE

WANT HALF MODE TO DRIVE MAIN SWITCH.

F HW PWM OFF ; STOP

HW PWM pwmhalf, polarity, dead band, period, duty
 USES A and C OUTOTS ↓ ↓ ↓ ↓
 0-127 0-255 0-1023

pwm HHHH ; A AND C ALWAYS SAME POLARITY
 | | | |
 A out
 D C B

IMPORTANT TO USE PULDOWN OR PULLUP RESISTOR ON OUTPUT, AS PIN WILL FLOAT BEFORE HW PWM ISSUED,

UNITS OF PERIOD ARE RESONATOR FREQ / 4, SO AT 32MHz,
 8 CYCLES = $1\ \mu s$, 256 CYCLES = $32\ \mu s$

PRECISION OF DUTY = $32\ nS$ @ 32 MHz

freq	max period	min duty	max dead time
32 MHz	$32\ \mu s$ (32 kHz)	$30\ nS$	$16\ \mu s$
div 4	$125\ \mu s$ (8 kHz)	$125\ nS$	$64\ \mu s$
div 16	$500\ \mu s$ (2 kHz)	$500\ nS$	$256\ \mu s$

EXPERIMENTS SHOW THAT WITH HALF BRIDGE, A IS OUTPUT AND B IS COMPLEMENT.
 ↴ 14M2 P#6

07 MAY 15; Roderick.

WRITE PROGRAM TO TEST known off 14M2 (FIRMWARE 6.A)

OBSERVATIONS:

- 1) "A" OUTPUT IS PER SPEC SHEET, CONTROLLED BY DUTY VALUE.
TYPE IN SPEC 1 PLACE,
NOT C OR D AS PER
- 2) "B" OUTPUT IS THE COMPLEMENT (~~(NOT C OR D AS PER)~~)
- 3) DEADTIME VALUE IS SUBTRACTED FROM WIDTH OF BOTH A AND B.

DEADTIME COUNT	ACTIVE TIME	
	A	B
0	24 μ s	7.94 μ s
10	22.8 μ s	6.8 μ s
20	21.6 μ s	5.44 μ s

DOES DIV4 WORK? YES, BUT DEADTIME DOES NOT SCALE
HIGH - OK HIGH - SAME AS HIGH
LOW - SAME PCBA, YES,
ALL WORK.

DOES POLARITY WORK? YES, TRIED ~~LOW~~ SAME PCBA, YES,
~~HIGH~~ SAME ALL WORK.

ARE OTHER PINS C, D ~~UNTOUCHED~~, CAN THEY BE USED FOR OUTPUTS? YES, TRIED FORCING C AND D TO BOTH 0, 1, LOOKS FINE.

NOTE: IF USING PWM DIV4, DEADTIME DOES NOT SCALE, BUT CAN GET MORE DEADTIME BY SLOWDOWN PROCESSOR WITH SETFREQ

12 MAY 2015; Roderick,

OPTOCOUPLED SELECTION

- WANT HIGH CURRENT CAPABILITY
- ACTUALLY, JFET WANT FAST RESPONSE TIME, 5μS OR LESS
- CURRENT TRANSFER RATIO, CTR, NOT IMPORTANT, BUT 100% WOULD BE NICE.
- COLLECTOR-EMITTER MUST TOLERATE 20V OR MORE
- CANNOT HAVE SEPARATE V_{CC}

RESPONSE TIME IS LIKELY LIMITING CRITERION.

SHF618A $t_{on} = 6\mu s$ TYP

V0618A - $t_{on} = 2\mu s$ TYP

AH38 $t_{on} = 5\mu s$

X AH35 $t_{on} = 10\mu s$ TOO SLOW

SFH6156 3.2μs

SFH610 5.8μs

SFH620A } 3μs
SFH620B }

SFH617A 3μs

SFH610A 3μs

AH28 } ABOUT 5μs? CHEAP

AH25, 26, 27 }

AH35, AH37 } ~ 8μs X
VOM617-27 } - NO SAT $t_{on} = 6\mu s$, $R_L = 100 \Omega$ 3
V0610A }

CN717 } $t_{on} = 3\mu s$
SFH606 } $t_{on} = 20\mu s$
FROM
SATURATION.

CLOUD OK.

PS2501 - EBay - 5μs
@ 100Ω R_L

22 MAY 2015

H_PWM SYNCHRONOUS RECT EXPERIMENT - 2

Roderrick

TRIED CIRCUIT w/ h_pwm B NOT CONNECTED.

AS EXPECTED, FET VOLTAGE LEVELS OFF AT ABOUT 20V,

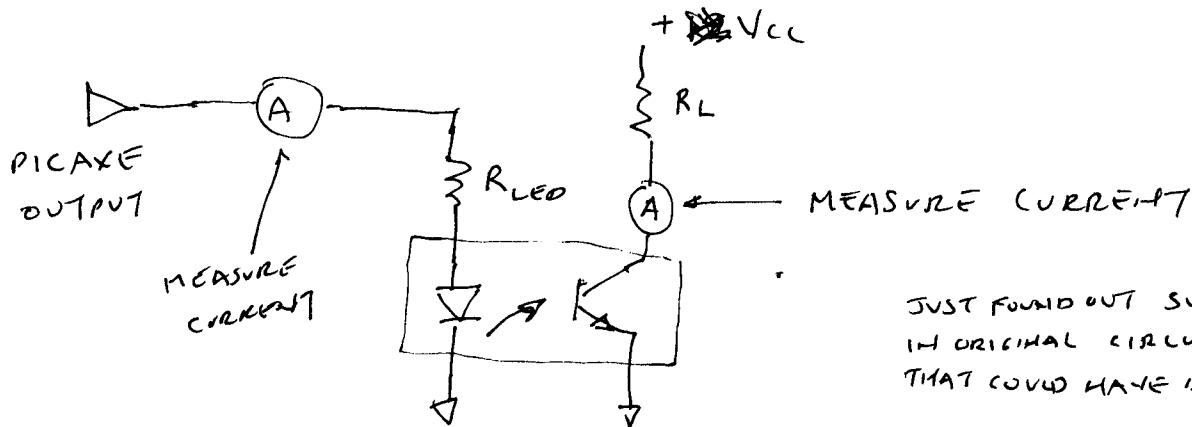
SCOPE SHOWS MAIN INDUCTOR ZOOMING BACK TO -24V, A SIGN THAT SYNC RECT NOT TURNING OFF.

CONNECTED UP h_pwm B, NO EFFECT, COULD BE THAT OPTOISOLATOR IS TAKING TOO LONG TO TURN ON. WILL TRY REDUCING DEADTIME.

$$\text{deadtime} = 20 \quad (5\mu\text{s})$$

$$\text{duty} = 400 \quad (25\mu\text{s})$$

15 JUN 2015, OPTOISOLATOR DOESN'T APPEAR TO BE MAKING ANY DIFFERENCE. COULD BE THAT I'M USING PARTS SALVAGED FROM OLD POWER SUPPLY, AND I BURNED THEM OUT DURING DESOLDERING? ONLY TRIED ONE PART. SHOULD RUN STATIC TEST ON ALL 3:



JUST FOUND OUT SUPPOSED 262Ω
 IN ORIGINAL CIRCUIT WAS 2.62k
 THAT COULD HAVE BEEN IT.

$$R_{L\text{ED}} = 262\Omega, R_L = 262\Omega \text{ ALSO. } V_{CC} = 5V$$

PUT TOP OF R_L_{ED} DIRECTLY TO +5V INSTEAD OF PICAXE OUTPUT.

MEASURED 13.1 mA AT OUT PUT. INPUT CURRENT - 13.7 mA

SOURCED FROM PICAXE OUTPUT, 10.7 mA

TRY OTHER PARTS, 262 DIRECT TO 5V OUT

PART A: 13.24 mA, B: 13.78 mA, C: 13.28 mA → CONCLUDE:
 PARTS NOT DAMAGED.

HPWM SYNCHRONOUS RECT EXPERIMENT,

PROGRAM PUTS OUT BURST OF PULSES THROUGH hpwm

setfreq m16

deadtime: 40 → 10 μs

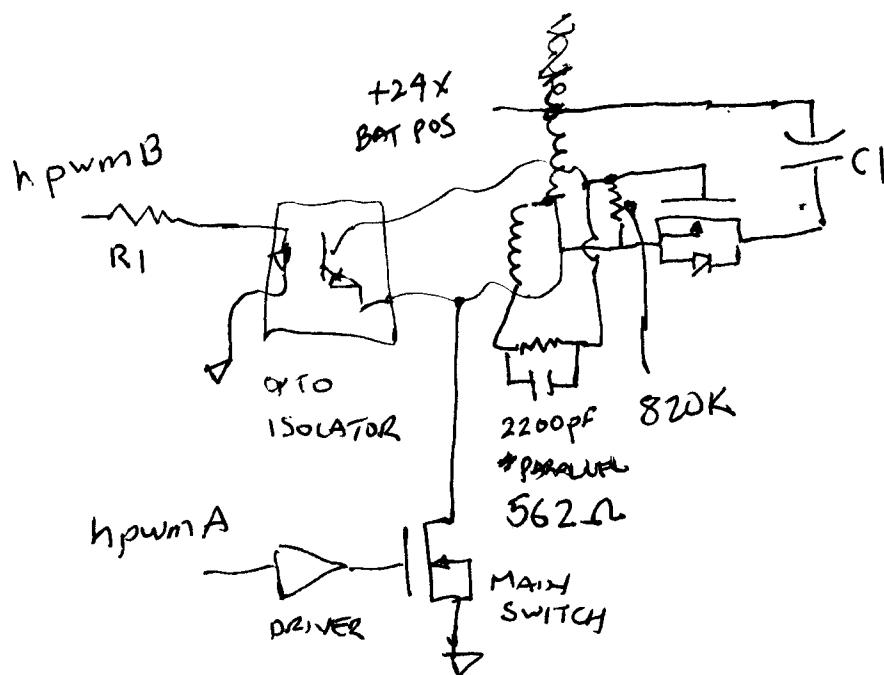
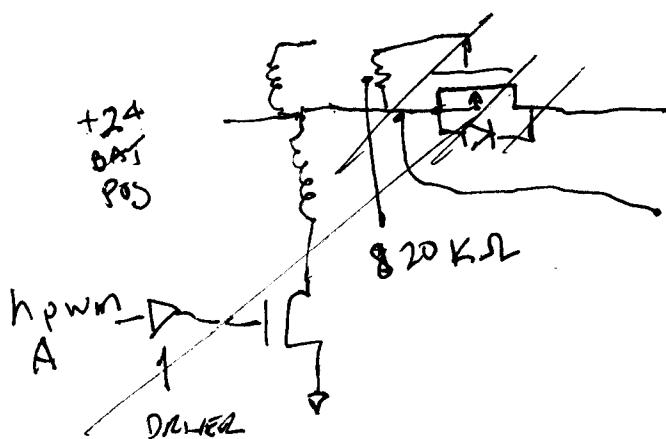
periodd = 255 → 4 μs

duty = 480 → 30 μs ~~PERIOD~~^{DUTY} - 10 μs DEADTIME = 20 μs PULSE

hpwm pwmhalf, pwmHHHH, deadtime, period, duty

pause 48

hpwm off

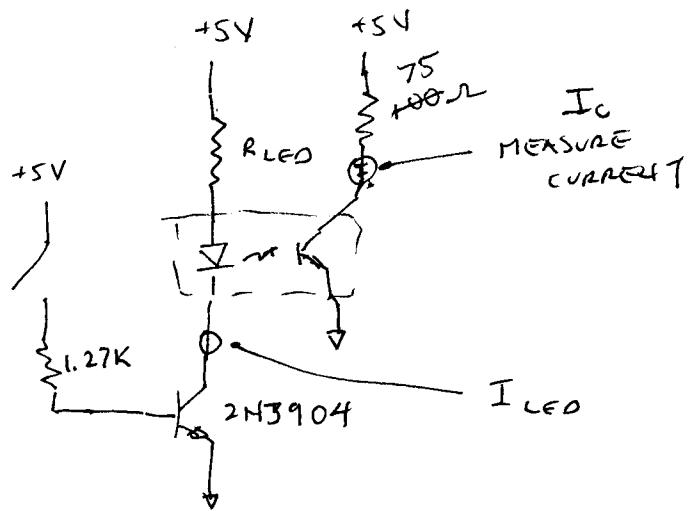


FIRST, TOOK TRACE

WITH GATE OF SYNC
RECT JUST TIED TO
GROUND.

VOLTAGE ACROSS C1
TAKES ~ 10.1 ms
TO GO 0 → 30.0 VOLTS

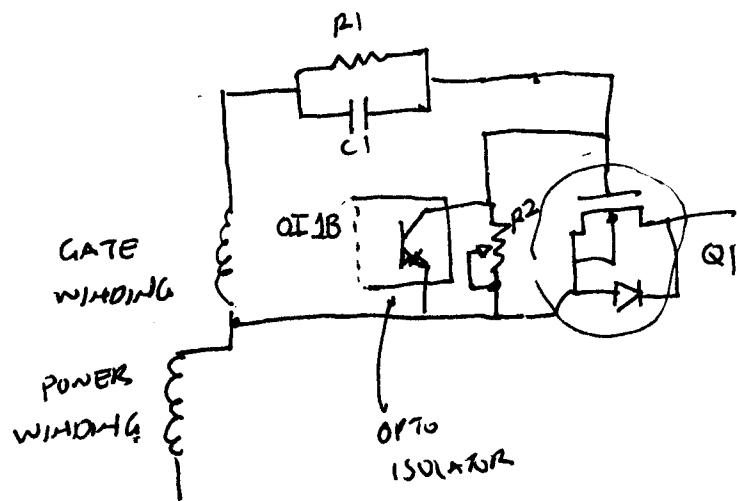
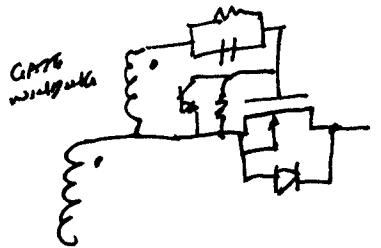
TRY:



$$R_{LED} = 137\Omega, \quad I = 24.7 \text{ mA} \quad I_{LED} = 25.8 \text{ mA}$$

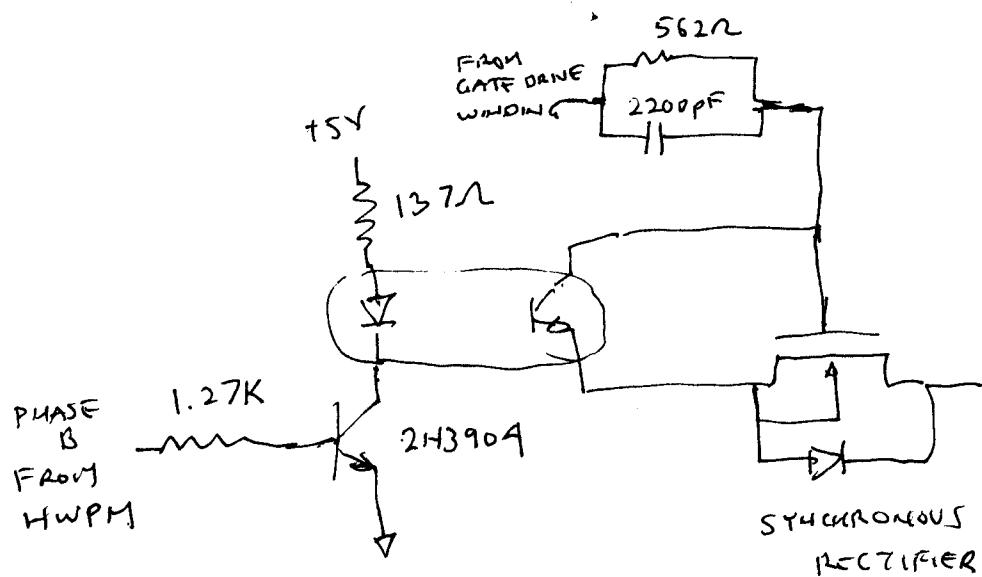
$$68\Omega \quad 29.3 \text{ mA} \quad 49.1 \text{ mA}$$

IN REAL CIRCUIT, CAN PUT MULTIPLE LEDs OR OPTOISOLATORS
IN SERIES.



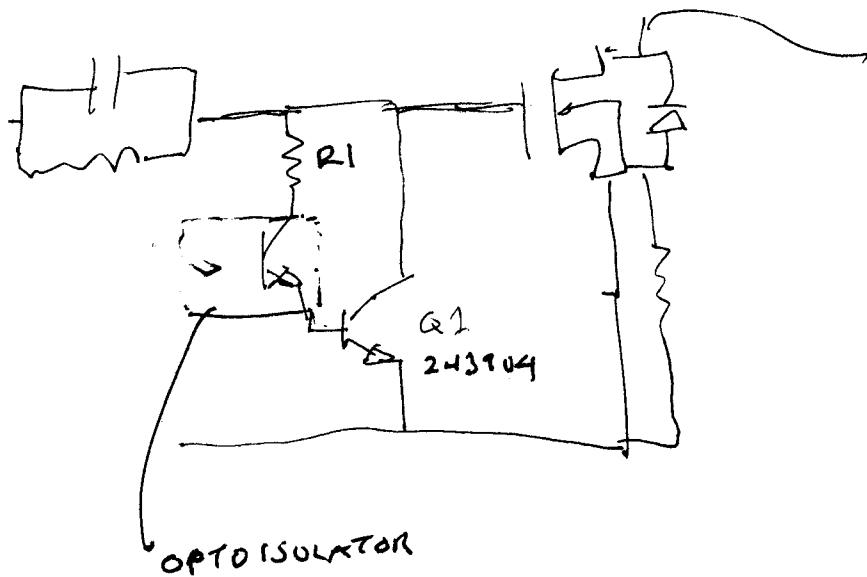
- ARRANGEMENT ALLOWS
1. USE OF OPTO ISOLATOR IF DESIRED
 2. PASSIVE PULSE LIMITING IF DESIRED
 3. DISABLE SYNC RECTIFIER IF DESIRED

TRIED EXPERIMENT, DRIVING SALVAGED P721F OPTOCOUPLED
TO TURN OFF SYNCHRONOUS RECTIFIER GATE PULSE. ~~USED~~



OPTOCOUPLER SEEKS TO STAY CONSTANTLY ON. THE SALVAGED ISOLATORS I
HAVE SAW "P721F" ON THEM - SUSPECT THEY ARE TLP721F. TOSHIBA
DATA SHEET DOES NOT EVEN SHOW SWITCHING CHARACTERISTICS -
PROBABLY DESIGNED ONLY FOR PRECISION CURRENT TRANSFER. LOOKING AT
OTHER PARTS

FOUND LOOSE DSC WIRE IN ATTIC



Q1 CAN'T
SATURATE —
DARLINGTON
ISSUE ACTUALLY
HELPS

$$R1 = 1.27k$$

NAKED OPTOISOLATOR DOES NOT HAVE ENOUGH DRIVE TO TURN OFF STAC RELT QUICKLY ENOUGH, ABOUT SAME AS PLAIN RESISTOR. TRY THE CIRCUIT ABOVE. STARTING TO GET COMPLICATED IF CIRCUIT MUST BE REPLICATED 12 TIMES — ONCE FOR EACH CELL.

RESULTS NOT GOOD. TOO OPTO GETS SATURATED IF IT CAN TURN ON AT ALL, THEN KEEPS Q1 ON TOO LONG. MAYBE TRY OPTO WITH DIODE OUTPUT?

09JUL2015;

Roderick.

OPTO ISOLATOR CUTOFF FOR GATE DRIVE DOES NOT SEEM PRACTICAL. FOR FAST OPTOCOUPERS, NEED A POWER SUPPLY, WHEN I USE JUST A PLAIN 4-PIN COUPLER, I WANT THE TRANSISTOR TO TURN ON HARD FOR GOOD RESPONSE. BUT THEN, IT SATURATES, AND WON'T TURN OFF IN THE TIME OF A FEW MS ALLOWED.

WILL GO WITH ENTIRELY PASSIVE GATE DRIVE.

HOW ABOUT USING A SCHOTTKY IN PARALLEL WITH TRANSISTOR? A SCHOTTKY DIDN'T WORK WELL BEFORE, BUT I NOW REALIZE I WAS PROBABLY EXCEEDING THE REVERSE VOLTAGE, AND IT WAS BREAKING DOWN, WILL TRY NTST30120CTG - 30A, 120V. FIRST, TAKE REFERENCE READING ON BODY DIODE CIRCUIT ALONE - NO SYNC RECT DRIVE, 4258 PULSES TO GET TO ADC VOLTAGE OF 100% IN PROGRAM

~ /PICAXE/rectifier_efficiency-test.bas.

256 PULSES WITH SCHOTTKY, TRY AGAIN. 245 PULSES, TRY AGAIN. 244. REMOVE SCHOTTKY (BODY DIODE ONLY), 252. AGAIN. 251. AGAIN. 246. AGAIN. 248.

→ SCHOTTKY RATED FOR 20V ONLY, PROBABLY BREAKDOWN